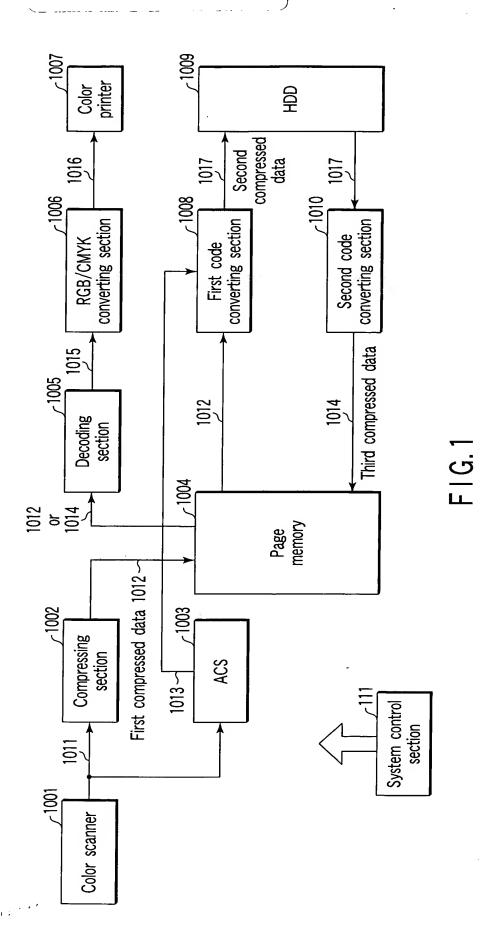
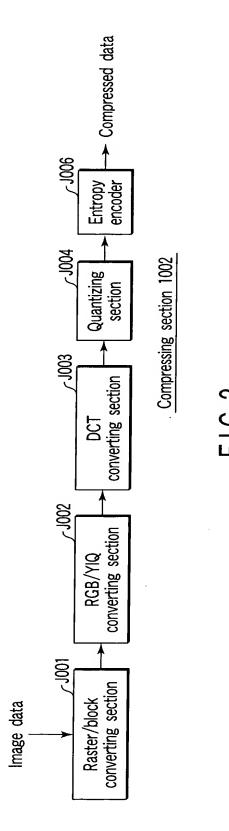
Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454



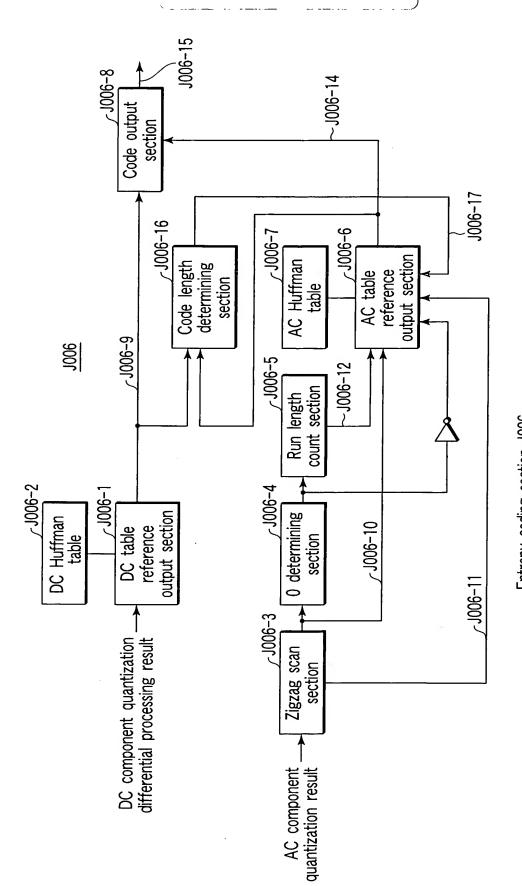


Title: APPARATUS FOR IMAGE PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454



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Appl. No.: 10/806,454

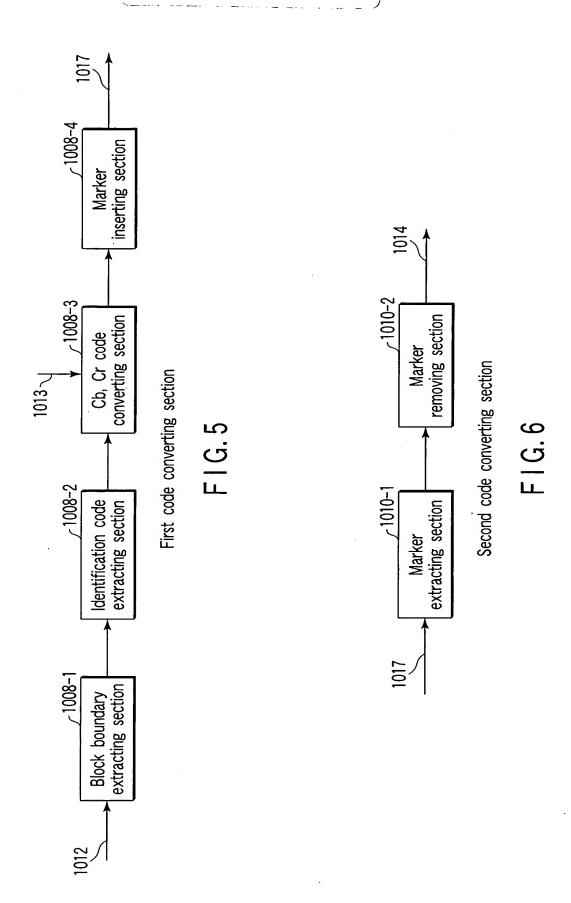


Entropy coding section J006

F1G.3

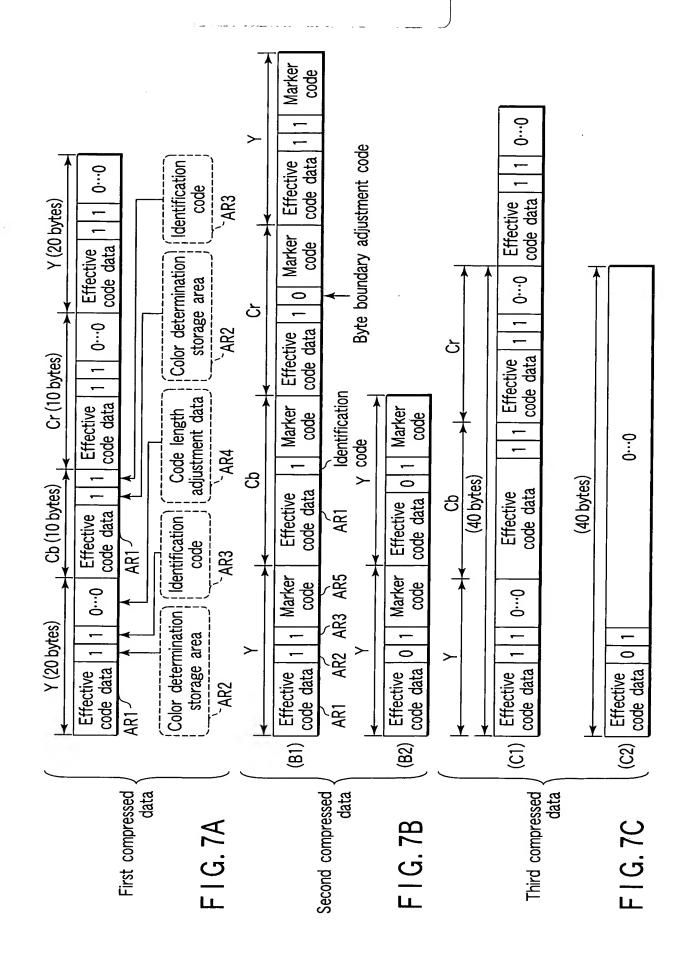
Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 1013 Comparator Threshold "2" -1003-3Counter Comparator Threshold "1" F1G.4 -1003-1 ACS 1003 Adder ABS-R ABS-G ABS-B Absolute Absolute value Absolute value value SUB-R -SUB-G SUB-B Differentiator Differentiator Differentiator 1011-G R 1011-R --മ

Title: APPARATUS FOR IMAGE PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454



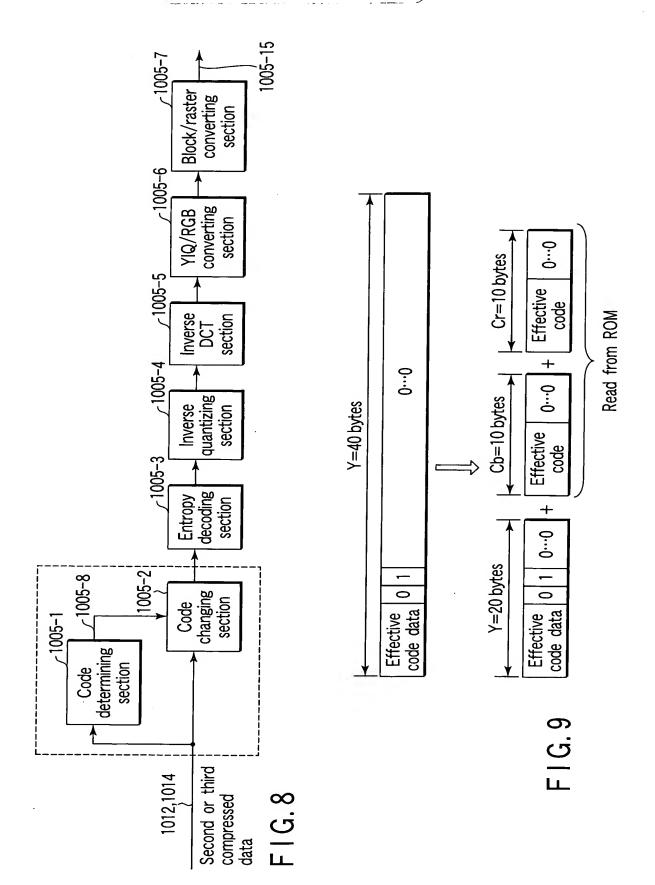
Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

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Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

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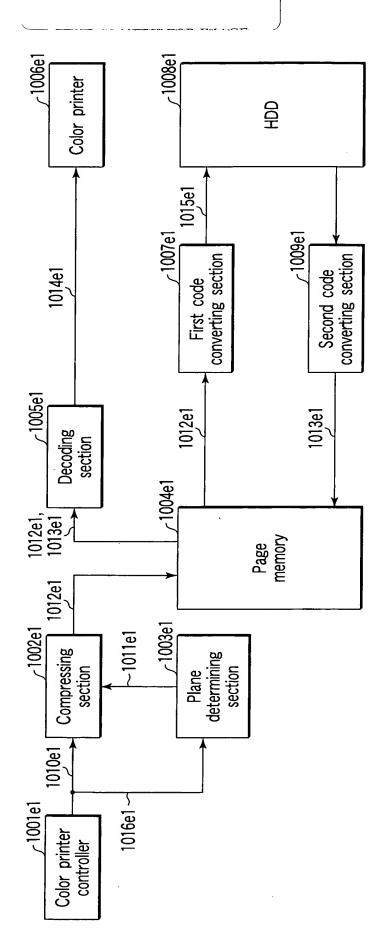


Title: APPARATUS FOR IMAGE PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454

1 Block ACS ACS

Output	Block determination	0	0	0	-	0		0
Input	Block ACS	0	-	0		0		1
	Entire ACS	0	0	-			ı	_
	Compression mode signal 1019	ACS	ACS	ACS	ACS	Color	Color	Monochromatic

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F1G.12

Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 → 1011e1-Y ≯1011e1-C → 1011e1-M ►1011e1-K -1003e1-10 1003e1-12 ·1003e1-11 Comparator Comparator Comparator Comparator F1G. 13 0 0 0 0 -1003e1-6 -1003e1-8 -1003e1-7 ADD ADD ADD ADD RST 71003e1-3 ~1003e1-2 71003e1-4 converting section converting section converting section converting section Raster/block Raster/block Raster/block Raster/block

` ;

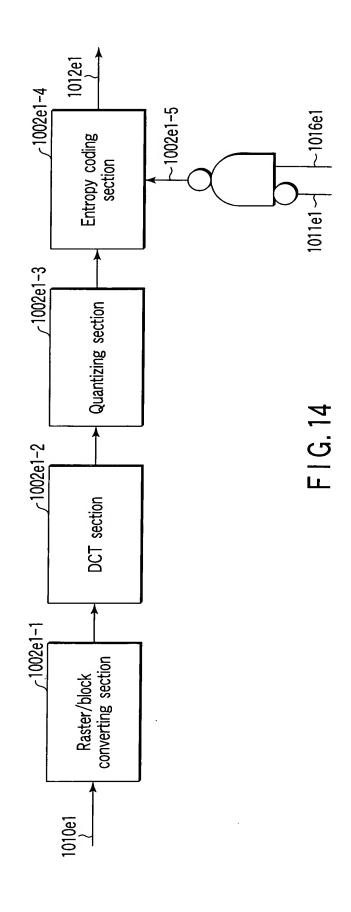
1003e1-9

·1003e1-5

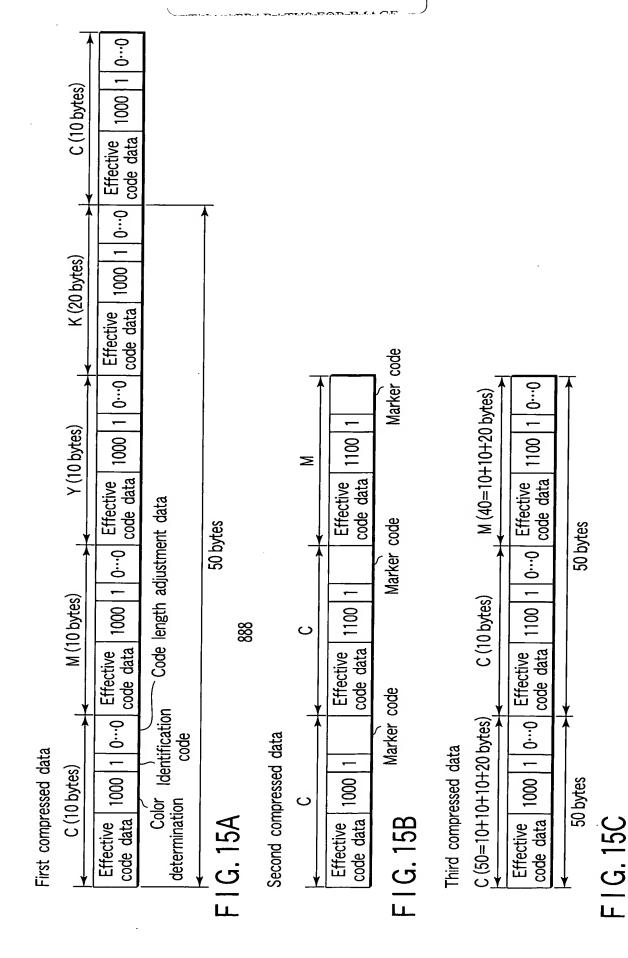
-1003e1-1

1010e1-M-

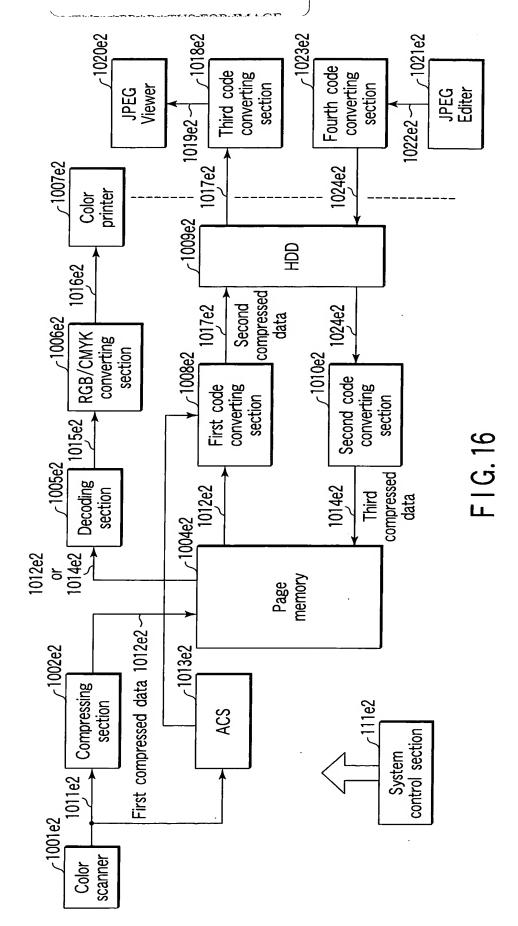
Title: APPARATUS FOR IMAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454



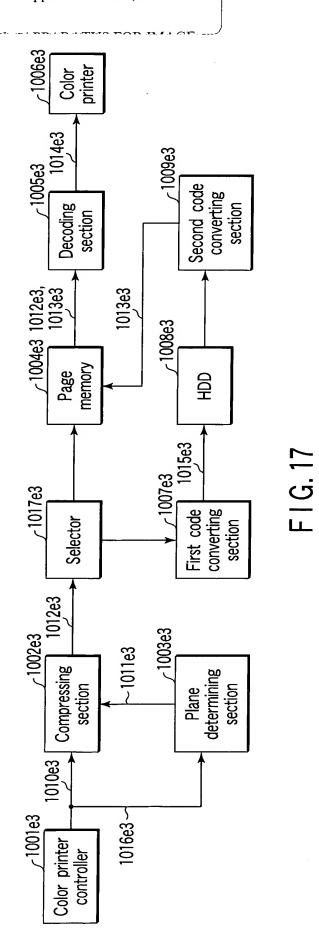
PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454



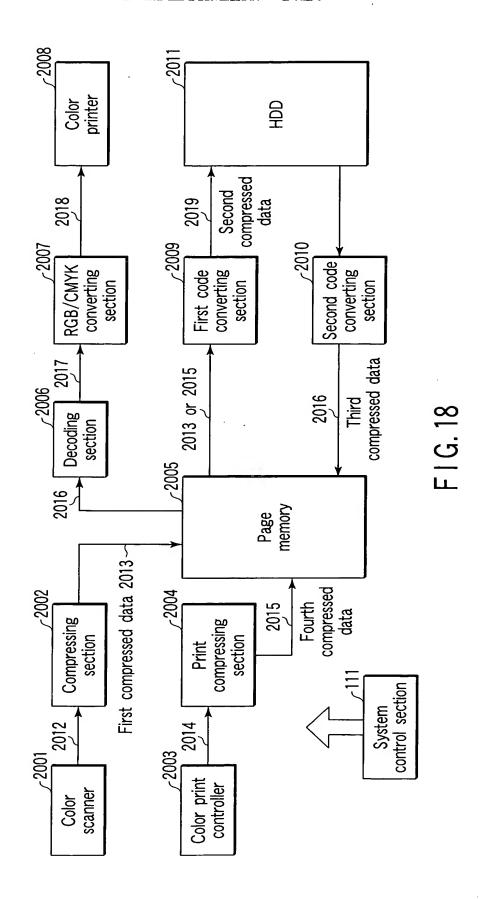
PROCESSING
Inventor(s): Sunao TABATA et al.
Appl. No.: 10/806,454



Title: APPARATUS FOR INTAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

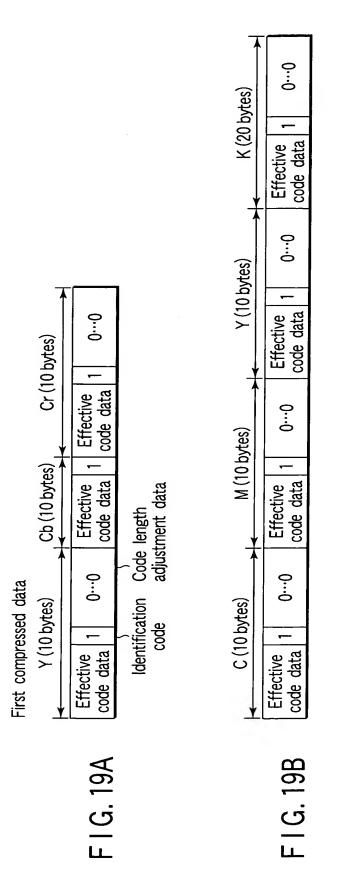


Title: APPARATUS FOR INTAGE PROCESSING Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454



PROCESSING
Inventor(s): Sunao TABATA et al.

Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454



0..0 Cr (20 bytes) Effective code data Cb (10 bytes) Effective code data 0...0 Y (20 bytes) Effective code data

FIG. 19C

Appl. No.: 10/806,454 ADDAMA TITLE TOD TAKACE 오 忍  $\mathbb{Z}$  $\mathcal{S}$ 조 50 bytes 9 50 bytes 50 bytes  $\chi$ 50 bytes  $\mathfrak{S}$  $\subseteq$ ප **W**2 S Ξ  $\frac{8}{8}$ 40 bytes 33 ප  $\Im$  $\mathbb{S}$  $\overline{\mathbf{c}}$ 72 0..0  $\geq$ □ WCU 0 ₩CU 2 € NOCN 3 ₩CU1 2 ට  $50\times4=200$  bytes ප OSW ↑ 엀 오  $\overline{\mathbb{Z}}$  $\overline{\Delta}$  $\mathfrak{Z}$ 72 Sampling for ratio of length to width of 1/2 9  $\frac{1}{2}$  $\mathcal{Z}$ င်  $\equiv$ 2  $\frac{9}{8}$ ∀  $\mathbb{R}$  $M_2$ ප FIG. 20D MCU ප  $\mathbb{S}$  $\Im$  $\Box$  $\mathfrak{S}$  $\equiv$ 2 FIG. 20B  $\infty$ σ വ ~ **∞** 

PROCESSING
Inventor(s): Sunao TABATA et al.

PROCESSING

Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

Print scan (data read direction)

Y0 Y1 Cb0 Cr0 Y0 Y1 Cb0 Cr0

Print scan (data read direction)

Y2 Y3 Cb1 Cr1

Y2 Y3 Cb1 Cr1

			-
	중	K	
	9	<u>۲</u>	
	용	M1	
	CO MO YO KO	C	
	중	C1 M1 Y1 K1 C1 M1 Y1 K1	
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	9	b1 (	
	1 C	3 C	
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	Y0 Y1 Cb0 Cr0 Y0 Y1 Cb0 Cr0	Y2 Y3 Cb1 Cr1 Y2 Y3 Cb1 Cr1	
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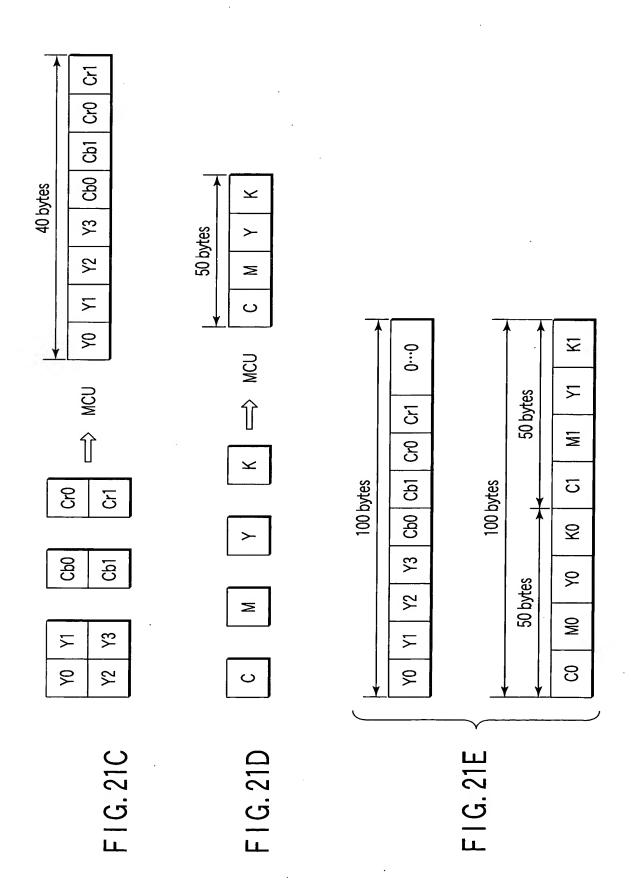
F1G.21A

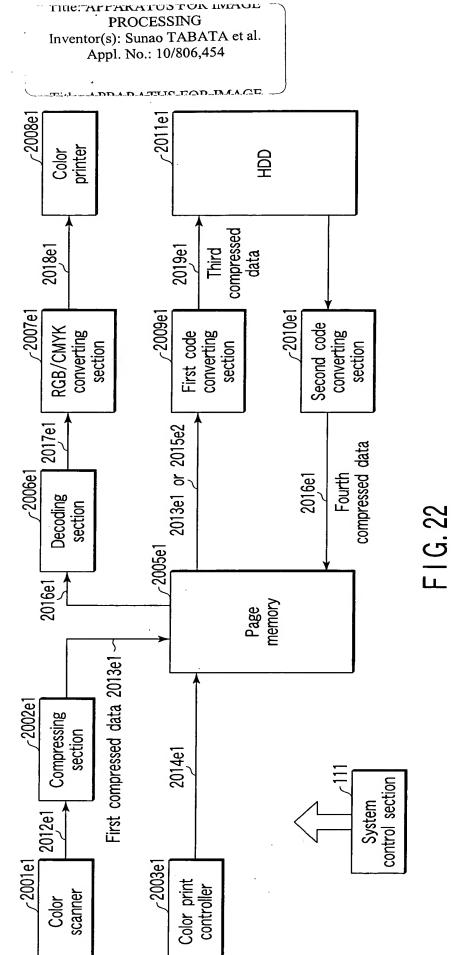
F1G.21B

TITIE: TAPPAKA POSTOK IIVIAOL PROCESSING

Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454

TO ALDO A DEATHIR EQUITALA GE





THIE: APPAKATUS TUKTIVIAUE **PROCESSING** Inventor(s): Sunao TABATA et al. Appl. No.: 10/806,454 ×64= 32 bytes 0...0 **63** 오 Y63 9 K63 4 bit M63 8 Y63 **C**63 ප 40 bytes  $\equiv$ **K**63 32 bytes  $\Box$ 2 중 F1G. 23C FIG. 23B M63 2  $\frac{8}{2}$ **C63** 8FIG. 23D 63

F1G. 23A  $\infty$ တ  $\infty$  $\infty$ 

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40 bytes

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